

### **Remarks/Arguments**

#### ***Summary***

By this Amendment, claims 5, 6 and 9-11 have been canceled, claims 1-4, 7-8, 13 and 17 have been revised, and new claims 20-25 have been added.

Accordingly, claims 1-4, 7-8 and 12-25 are now pending in the application.

#### ***Power of Attorney and Change of Correspondence Address***

On January 10, 2003, the undersigned filed a "Submission Of Power Of Attorney And Change Of Address" and a "Power Of Attorney". Copies of these papers, and a returned postcard receipt, are attached.

Please make a record of the new correspondence address and the change in power of attorney in this application.

#### ***Allowable Subject Matter***

Applicants acknowledge with thanks the indicated allowability of dependent claims 2-6, 8 and 10, and the indicated allowance of claims 12-19.

#### ***35 U.S.C. ¶102***

Claims 1, 7, 9 and 11 were rejected under 35 U.S.C. ¶103 as being anticipated by Rice et al. (US 4510465). Applicants respectfully traverse this rejected with respect to the now-pending claims.

One characteristic of the present invention according to claim 1 resides in "a PLL circuit which detects a phase difference between an input signal and a reference signal wherein the PLL outputs a signal of which an undesired frequency component is removed; an AGC circuit which controls a gain of modulating signal based on the signal outputted from the PLL and outputs a control signal". One characteristic of the present invention according to new claim 20 resides "a PLL circuit which detects a

phase difference between an input signal of which a frequency is divided and a reference signal; an AGC circuit which controls a gain of a modulating signal based on the input signal of which a frequency is divided, and which outputs a control signal”.

On the other hand, cited reference Rice et al. reference discloses a PLL circuit (phase comparator 35, loop filter 45), an AGC circuit (audio compensator 65) and a voltage controlled oscillation circuit (voltage controlled oscillator 55). As shown in Fig. 4, an output of the loop filter 45 is input to the voltage controlled oscillator 55, not the audio compensator 65.

Rice et al. does not disclose a PLL circuit which detects a phase difference between an input signal and a reference signal wherein the PLL outputs a signal of which an undesired frequency component is removed; an AGC circuit which controls a gain of a modulating signal based on the signal outputted from the PLL and outputs a control signal”. Further, Rice et al. does not disclose “a PLL circuit which detects a phase difference between an input signal of which a frequency is divided and a reference signal; an AGC circuit which controls a gain of a modulating signal based on the input signal of which a frequency is divided, and which outputs a control signal”.

Thus, Applicants respectfully contend claims 1 and 20, and the claims dependent thereon, are not anticipated by Rice et al.

***Conclusion***

No other issue remaining, reconsideration and favorable action upon the claims now present in the application is respectfully requested.

Respectfully submitted,

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